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## APPLICANTS

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## \*\* CONTINUING DATA \*\*\*\*\*

JMJ

This application is a DIV of 09/982,413 10/17/2001 PAT 6,917,069

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JMJ

None

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/07/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Jonas M. Thomas</i> JMJ Examiner's Signature Initials	CA	18	24	2

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94303-2248

## TITLE

Self aligned method of forming a semiconductor memory array of floating gate memory cells with buried bit-line and vertical word line transistor

FILING FEE  RECEIVED	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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